

Analog IC Designer (Full-time contract basis) at IITH within the R&D project.

Number of positions: 1 no.

Domain: Analog and Mixed-Signal Integrated Circuit Design

Key Responsibilities:

- Design and develop analog and mixed-signal integrated circuits at the transistor level.
- Perform schematic design, testbench development, and pre-layout simulations using Cadence Virtuoso, Spectre, and ADE.
- Design analog blocks such as Op-Amps, comparators, current mirrors, bias circuits, bandgap references, and LDOs.
- Conduct DC, AC, transient, noise, stability, PVT corner, and Monte Carlo simulations to verify circuit performance.
- Optimize circuits for power, performance, area (PPA), gain, bandwidth, noise, and stability.
- Collaborate with layout engineers on floorplanning, matching techniques, and layout reviews.
- Perform post-layout simulations with parasitic extraction and debug performance deviations.
- Work with foundry PDKs, device models, and process design rules to ensure design compliance.
- Collaborate with cross-functional teams to support design reviews, documentation, silicon characterization, and validation.

Required Skills: Strong fundamentals in CMOS analog circuit design, transistor-level simulation, analog performance analysis, PVT and Monte Carlo analysis, and hands-on experience with industry-standard analog design EDA tools.

Preferred Qualification: Looking for a mandatory PhD with Btech/ Mtech in Electrical Engineering, Electronics and Communication Engineering, or VLSI Design, with 3+ years of experience

Salary: Rs. 50000 /- per month with HRA.

Contract Period: 1 year and will be extended based on their performance and requirements.

Job Location: IIT Hyderabad, IC Design LAB.

Joining Date: 20th November 2026

How to apply: Kindly drop your latest resume at prahaseeth.manne@ee.iith.ac.in on or before 10th November 2026.

Last date to receive CV is by 10th November 2026.