



5G/6G Research Projects at IIT Hyderabad

Applications are invited from the eligible candidates for the below positions from eligible candidates purely on ad hoc basis in the project

Advertisement for Project Associate role in 5G/6G Project IIT Hyderabad

1.	Name of post	Project Officer Engineer 3
2.	Job Description	 Software/hardware design expert with B. Tech or M. Tech with an experience of 5 years'. The tasks performed are development of design, algorithms, and development/implementation of Layer 1,2,3 software. Also, development of system-level-simulation tools and development of RTL on FPGA boards and embedded software. Testing of developed software and FPGA based solutions with signal generation/analysis and protocol testers. Also, mentoring the juniors, Engineers.
3.	Essential Qualification	B tech / BE/ M tech ME / MS Master's Bachelor's degree Electrical /Electronic engineering, Computer engineering, /MCA/MSC/Bsc/ BCA , Bachelor of Science or Equivalent practical experience
4.	Skills /Experience Required	 Domian Knowledge on RTL Design, implementation, and integrations for FPGA Designs. Experience with Intel or Xilinx FPGAs, especially MPSoC FPGA Knowledge with RTL coding using Verilog/System Verilog. Architecture exploration and Micro-architecture development RTL design and integration for 5G NR UE systems using Verilog/System Verilog Strong debugging capabilities at RTL simulation and FPGA Emulation Design & develop a 5G L2/L3 protocol stack (RAN & UE). Excellent C programming skills with strong debugging and troubleshooting skills over the Linux platform.

		 A good understanding of Networks, Network Protocols & hands-on experience with UDP/TCP/IP is required
5.	Consolidated Pay	1,50,000 to 2,28130
6.	Tenure of employment	Contract period of 11 months. However, candidate can be reengaged for subsequent terms based on his/her performance and requirement of the Department/Institute.
7.	Mode of selection	Test and interview
8.	Selection Procedure/Mode of application	1 Interested applicant should apply online through the online google form link . https://docs.google.com/forms/d/1NdqVI5WfUtqHcV1Pb5nbxwKXvhOpBlxP0eNMqk0 BbIA/edit 2.The candidate will be shortlisted based on the eligibility criteria, academic record, and relevant experience and selected based on the performance is the Witten interview. 3 Date and time of interview: this will be intimated to shortlisted candidates via email. 4 IIT Hyderabad reserves the right to alter the number of vacancies or keep it open till the deserving the right to close the recruitment procedure without
		selecting anybody, if none of the candidates are found suitable . 6. Candidates with prior experience in relevant areas will be given preference

9.	Tentative date of interview	Date & Time will be informed through mail to shortlisted candidates
10.	Contact	Candidate may contact: (Email:Srinivas.kvn@5g.iith.ac.in) carrers@5g.iith.ac.in