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## **Advertisement for JRF Post**

Position Name	Junior Research Fellow (JRF)
Number of Posts	1
Funding Agency	ISRO
Project	Design of transceiver for on board wireless interface of distributed control systems
Nature of work	Transistor level circuits design, analog layout in CMOS technology, PCB design and testing etc.
Duration	Two years (02)
Stipend	Rs. 37,000/- per month
HRA	As per the norms of the funding agency
Essential Qualifications	B.E./B.Tech. in Electrical Engineering or equivalent with more than 70% marks or equivalent CGPA. Valid GATE score is essential
Age Limit	Candidate should not be more than 25 years on application deadline
<b>Application Deadline</b>	Aug 18th, 2024
Application Method	Fill this google form <a href="https://forms.gle/NqkmS7YVd41kevwP9">https://forms.gle/NqkmS7YVd41kevwP9</a>
Required Documents	Originals of degree certificates (one set of xerox), date of birth proof, any other relevant information (like copies of publications, awards, recommendations etc.)
Interview Process	Shortlisted candidates will be informed via email and should appear in person/online for interview
Additional Notes	No travelling or other allowances for attending the interview
	Initially the JRF will be appointed for a period of 11 months and subsequently extended till the end of the project based on the performance.
Contact	office@ee.iith.ac.in