



భారతీయ సాంకేతిక విజ్ఞాన సంస్థ హైదరాబాద్
भारतीय प्रौद्योगिकी संस्थान हैदराबाद
Indian Institute of Technology Hyderabad

INDICATIVE SYLLABUS AND SCHEME OF EXAM

POST NO. 16.11.8-JUNIOR TECHNICIAN-ELECTRICAL ENGINEERING (MICROELECTRONICS & VLSI)

Ref: Notification No. IITH/2024/NF/16 dated 19.10.2024.

1. **Selection Process:** Selection Process consists of Written Test and Skill Test. Skill Test is qualifying in nature. There will be no weightage for skill test scores in final selection. Candidates must qualify in skill test for consideration in final selection. Final Selection will be based on the performance in the Written Test.
2. **Syllabus:**
 - i. **Basic Math and Communication Skills:** Arithmetic- Ratio and proportion, Arithmetic progression and geometric progression, Permutation and combination, Logarithm and exponential series, Complex numbers. General English- Parts of speech, Types of sentences, Numbers, genders, persons, tenses, articles and degrees, Direct and indirect speech, Active and passive voice.
 - ii. **Facility management:** Basics of vacuum technology, principles of HVAC and building management systems(BMS), Types of sensors/gauges, connectors and interfaces, ESD fundamentals, Computer skills: Basic programming skills (C and Python), pseudo-code.
 - iii. **Skill Test:** If selected, the candidate will be asked to demonstrate essential skills to perform the above job functions in the laboratories at IIT Hyderabad.
