

Date: 24-12-2024

**CORRIGENDUM-1**

<b>Tender Ref No</b>	IITH/EE/ASUDEB/2024/L/T045
<b>Tender ID</b>	2024_IITH_837860_1
<b>Name of Tender</b>	Engineering layout design for chip
<b>Reason of Corrigendum</b>	Changes in Bid submission end date & Bid opening date

<b>Description</b>	<b>Existing</b>	<b>Amended To</b>
1. Changes in Bid submission end date & Bid opening date	Bid Submission End Date: 25-Dec-2024 02:00 PM Bid Opening Date: 26-Dec-2024 02:30 PM	Bid Submission End Date: 30-Dec-2024 04:00 PM Bid Opening Date: 31-Dec-2024 04:00 PM

**Note:** All other terms and conditions of the tender remain unchanged. Bidders, who have already submitted their bids prior to issue of this corrigendum need to submit again.