

Indian Institute of Technology Hyderabad

Kandi, Sangareddy - 502 285, Telangana, India

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Date: 24-12-2024

CORRIGENDUM-1

Tender Ref No	IITH/EE/ASUDEB/2024/L/T045	
Tender ID	2024_IITH_837860_1	
Name of Tender	Engineering layout design for chip	
Reason of Corrigendum	Changes in Bid submission end date	
	& Bid opening date	

Description	Existing	Amended To
1. Changes in Bid	Bid Submission End	Bid Submission End Date: 30-Dec-2024
submission end date & Bid	Date: 25-Dec-2024	04:00 PM
opening date	02:00 PM	Bid Opening Date: 31-Dec-2024 04:00
	Bid Opening Date: 26-	PM
	Dec-2024 02:30 PM	

Note: All other terms and conditions of the tender remain unchanged. Bidders, who have already submitted their bids prior to issue of this corrigendum need to submit again.